



5-15-06

IFW

Docket No.: 02008/105002
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Shinsaku Higashi et al.

Application No.: 10/814,603

Confirmation No.: 5472

Filed: March 31, 2004

Art Unit: 2123

For: TEST EMULATOR, TEST MODULE
EMULATOR AND RECORD MEDIUM
STORING PROGRAM THEREIN

Examiner: Not Yet Assigned

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT (IDS)

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

22511
PATENT TRADEMARK OFFICE

Dear Sir:

In accordance with 37 CFR 1.97, Applicant(s) hereby make of record the following additional documents. A PTO Form SB/08 and a full copy of each of the documents required under 37 CFR 1.98(a)(2) accompany this statement.

Applicant(s) have become aware of the documents cited in a Supplementary European Search Report issued March 31, 2006, during the prosecution of European Application No. EP 04 72 4396, and the documents cited in a U.S. Office Action mailed on February 22, 2006, during the prosecution of the U.S. Application No. 10/404,002, which correspond to the above referenced application, and in accordance with 37 CFR 1.97(c) and (e)(1) or (b)(3), hereby submit(s) these documents for the Examiner's consideration. These documents are cited on the enclosed PTO Form SB/08, and copies of the Supplementary

European Search Report and the U.S Office Action and of each document required under 37 CFR 1.98(a)(2) cited thereon are enclosed as well.

This statement is not to be interpreted as a representation that the cited documents are material, that an exhaustive search has been conducted, or that no other relevant information exists. Nor shall the citation of any document herein be construed *per se* as a representation that such document is prior art. Moreover, Applicant(s) understand(s) the Examiner will make an independent evaluation of the cited documents.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned (37 CFR 1.97(b)(3)).

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith to our Deposit Account No. 50-0591, under Reference No. 02008/105002.

Dated: May 12, 2006

Respectfully submitted,

By 

Thomas K. Scherer
Registration No.: 45,079
OSHA · LIANG LLP
1221 McKinney St., Suite 2800
Houston, Texas 77010
(713) 228-8600
(713) 228-8778 (Fax)



Application No. (if known): 10/814,603

Attorney Docket No.: 02008/105002

Certificate of Express Mailing Under 37 CFR 1.10

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail, Airbill No. EV 809551974 US in an envelope addressed to:

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

on May 12, 2006
Date

Jhri Dern
Signature

Signature

Yuki Tsukuda

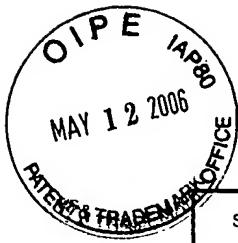
Typed or printed name of person signing Certificate

N/A

(713) 228-8600
Telephone Number

Note: Each paper must have its own certificate of mailing, or this certificate must identify each submitted paper.

IDS (Citation) by Applicant (9 References) (1 page)
Supplemental Information Disclosure Statement (2 pages)



Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/B/PTO				Complete if Known	
				Application Number	10/814,603-Conf. #5472
				Filing Date	March 31, 2004
				First Named Inventor	Shinsaku Higashi
				Art Unit	2123
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	02008/105002

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
AA*	US-6,026,230	02/2000	Lin et al.		
AB*	US-5,841,967	11/1998	Sample et al.		
AC*	US-6,009,256	12/1999	Tseng et al.		
AD	US-2001/016922 A1	08-23-2001	Takasugi et al.		

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)			
					T ⁶

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. * CITE NO.: Those application(s) which are marked with an single asterisk (*) next to the Cite No. are not supplied (under 37 CFR 1.98(a)(2)(iii) because that application was filed after June 30, 2003 or is available in the IFW. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
	CA	U.S. Office Action issued for U.S. Application No. 10/404,002 mailed on February 22, 2006, 13 pages			
	CB	Sunburst-Design.com. "Sunburst Design - Introduction to Verilog-2001 Design", http://www.sunburst-design.com/verilog_training/Intro_Verilog_Training.pdf . 2/2003			
	CC	Donna Mitchell, "Manual and Automatic VHDL/Verilog Test Bench Coding Techniques", 2001, http://www.omimo.be/magazine/01q2_p027.pdf			
	CD	Supplementary European Search Report issued for EP 04 72 4396 mailed on March 31, 2006, 3 pages			
	CE	Xia J.Q. et al: "Dynamic Test Emulation for EDA-Based Mixed-Signal Test Development Automation" Proceedings of The International Test Conference (ITC). Washington, Oct. 21-25, 1995 (1995-10-21), pages 761-770, XP000552879 ISBN: 0-7803-2992-9			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature		Date Considered
--------------------	--	-----------------